



IFW

PATENT

Attorney Docket No. YOR920030290US1 (8728-636)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE


In re application of: Azeez Bhavnagarwala Group No.: 2827
Serial No.: 10/649,200 Examiner: Connie C. Yoha
Filed: August 27, 2003 Date of mailing of "Notice of Allowance and
Base Issue Fee Due" March 17, 2005
Confirmation No. 5141
For: LOADLESS NMOS FOUR TRANSISTOR DYNAMIC DUAL VT SRAM CELL

MAIL STOP ISSUE FEE
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

TRANSMITTAL OF FORMAL DRAWINGS

Attached please find four (4) sheets of formal drawings for the above-identified application.

Please charge any deficiency as well as any other fee(s) which may become due under 37 C.F.R. § 1.16 and/or 1.17 at any time during the pendency of this application, or credit any overpayment of such fee(s) to Deposit Account No. 50-0510/IBM. TWO (2) COPIES OF THIS SHEET ARE ENCLOSED.


SIGNATURE OF ATTORNEY

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(Transmittal of Formal Drawings [5-2])